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P2002,0624

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By: Vanglong Chen

Date: May 24, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/626,955 Confirmation No. 3783  
Applicant : Patrick Heyne  
Filed : July 25, 2003  
TC/A.U. : 2827  
Examiner : David Lam  
Title: : INTEGRATED SYNCHRONOUS MEMORY AND MEMORY  
CONFIGURATION HAVING A MEMORY MODULE WITH AT  
LEAST ONE SYNCHRONOUS MEMORY

Docket No. : P2002,0624

Customer No. : 24131

Hon. Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

S i r :

In response to the Office action dated March 11, 2005, please  
amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of  
claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.